

Introduction to M76H

M76H, integrated with heterogeneous multi-core NPU V3 architecture, supports low-bitwidth mixed precision, featuring high-efficiency tensor cores optimized for CNNs and accelerated performance for Transformer and BEV applications. The flexible vector cores are specifically designed to enhance Transformer networks, in synergy with SDMA and heterogeneous multi-core hardware coordination. With a rich set of CV operators for pre- and post-processing, M76H significantly enhances perception capabilities.

- 8-core high-performance CPU, dual-core vision DSPs, and support for VSLAM and radar data processing.
- M76H also excels in video access and processing, efficient memory bandwidth, and powerful AI computation, enabling the simultaneous handling of multiple high-resolution video streams.
- The domestic domain control platform for driving and parking with lease power consumption in the industry features passive cooling to reduce system costs. This platform supports comprehensive solutions for full-time driving and parking systems, enabling high-speed NOA, urban ICA, automated parking, and memory parking.

Future Prospects:

The automotive main control chip M76H meets the demands of various scenarios with its powerful sensing, computing, and data processing capabilities. Boasting industry-leading capabilities in both BEV and Transformer models and the only passive cooling domain control for full-time driving and parking system in the industry, M76H enables high-speed NOA, urban ICA, and automated parking. This promising chip will play a significant role in smart driving, leading the development and innovation of domestic automotive chips.